

Fig. 1 PRIOR ART

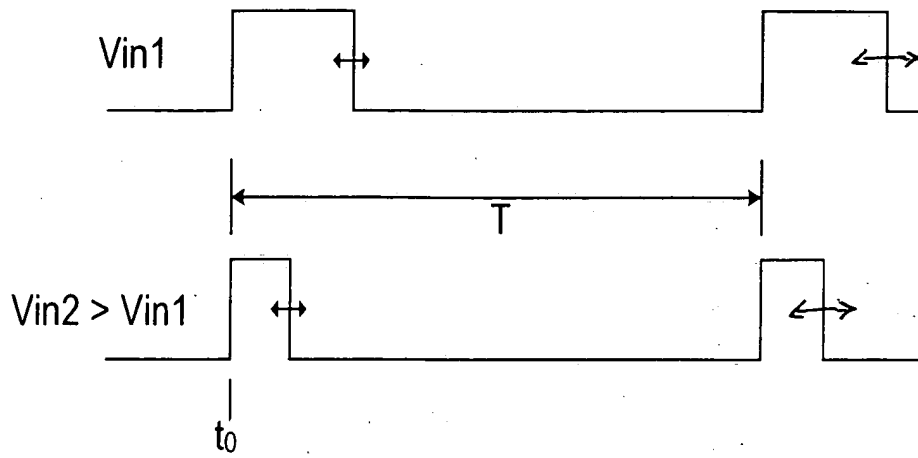


Fig. 2 PRIOR ART

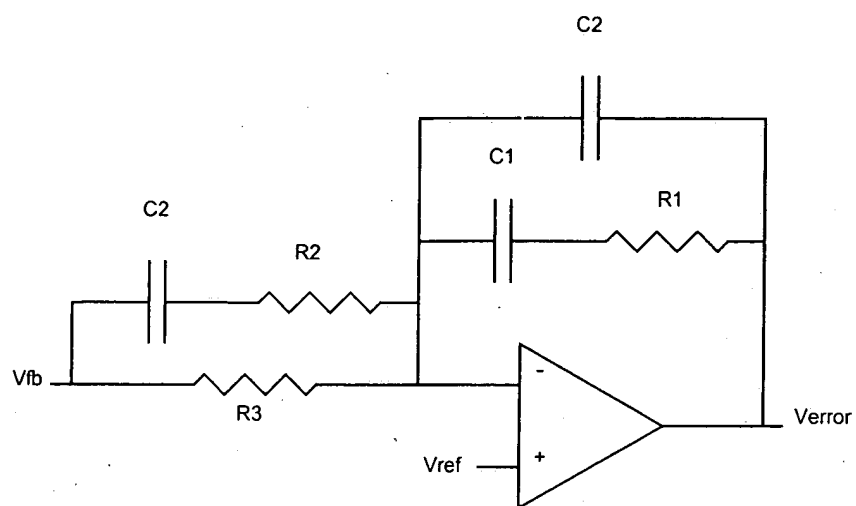


Fig. 3 PRIOR ART

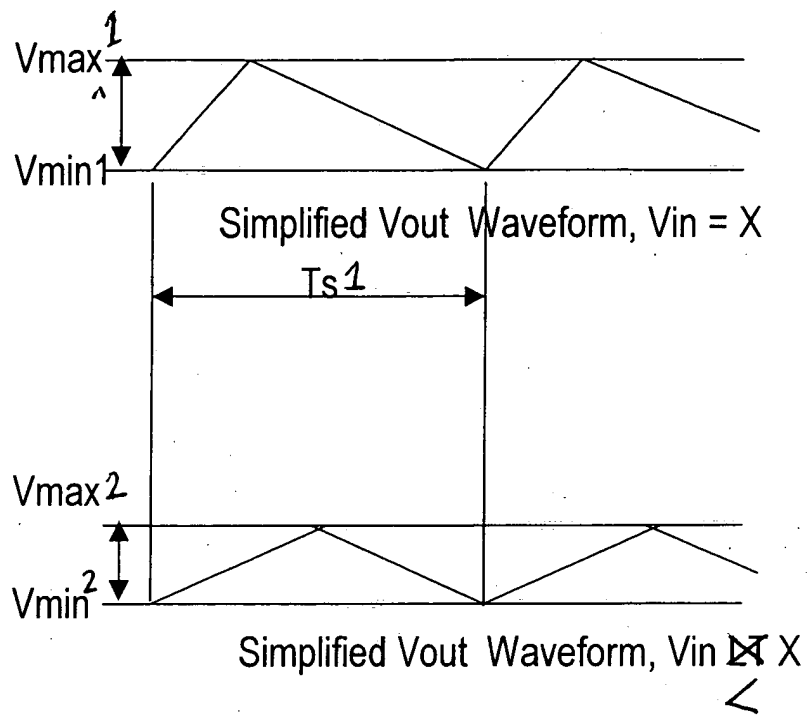


Fig. 4 ~~Fixed Frequency Hysteretic Regulator~~
~~Output Ripple Voltage~~
 ~~V_{max} — V_{min} variable, f fixed~~

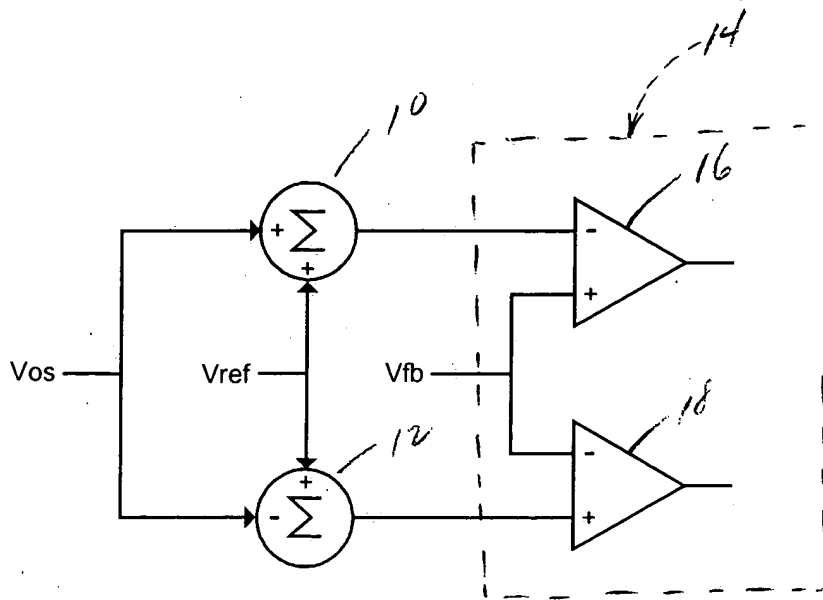


Fig. 5 ~~Typical Standard Hysteretic Control Circuit~~
 PRIOR ART

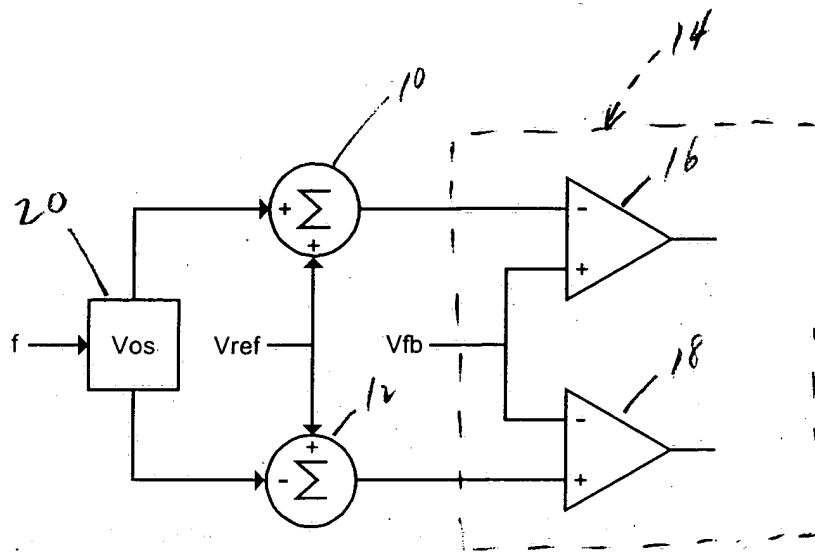
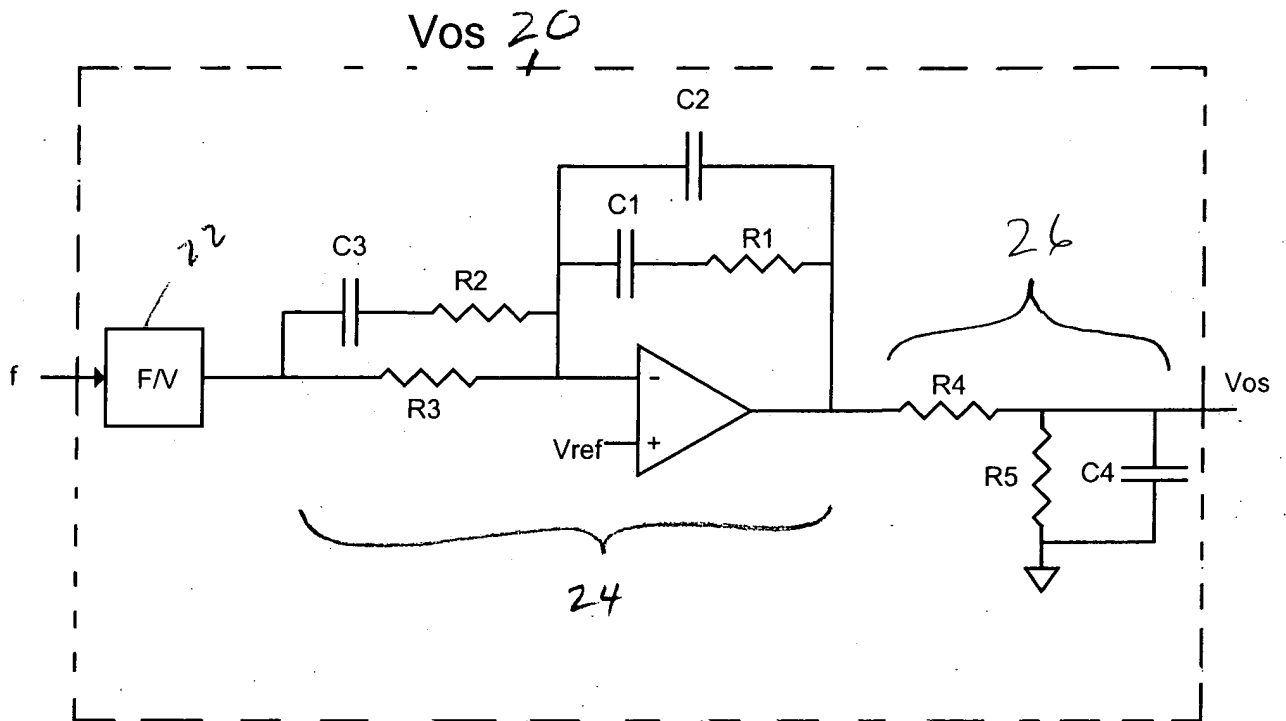


Fig. 6 Simple Fixed Frequency Hysteretic Control

Fig. 7 ~~Offset Voltage Circuit~~



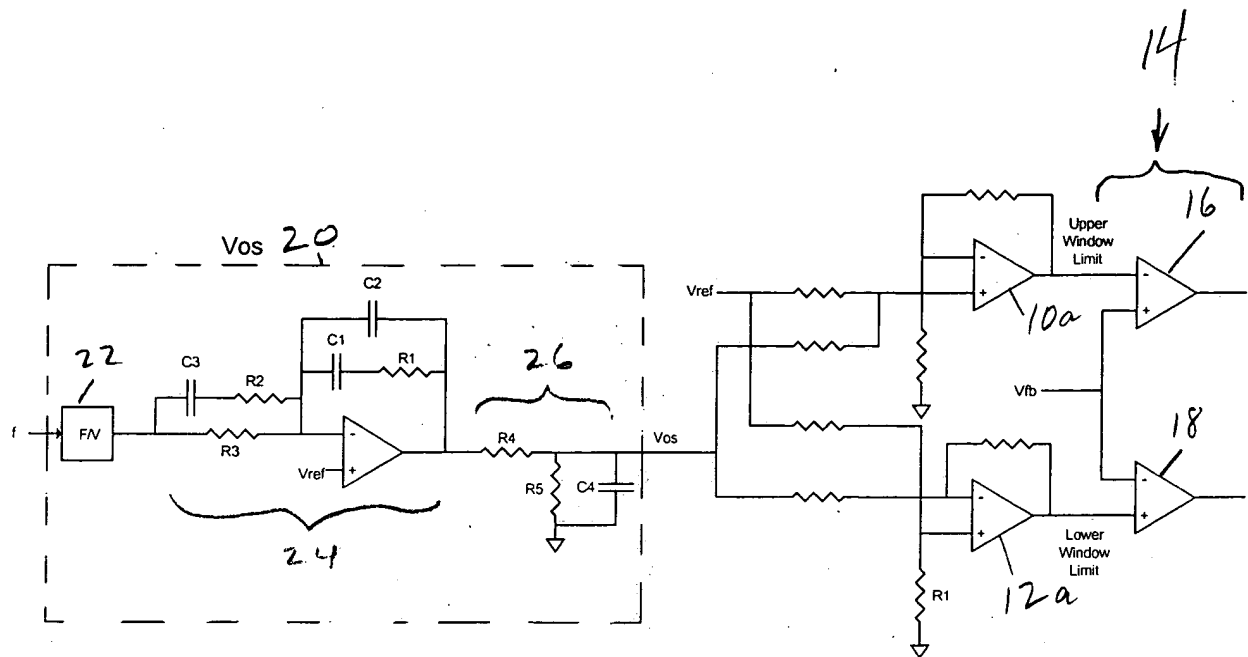


Fig. 8 ~~Circuit Implementation of Simple FFHC~~

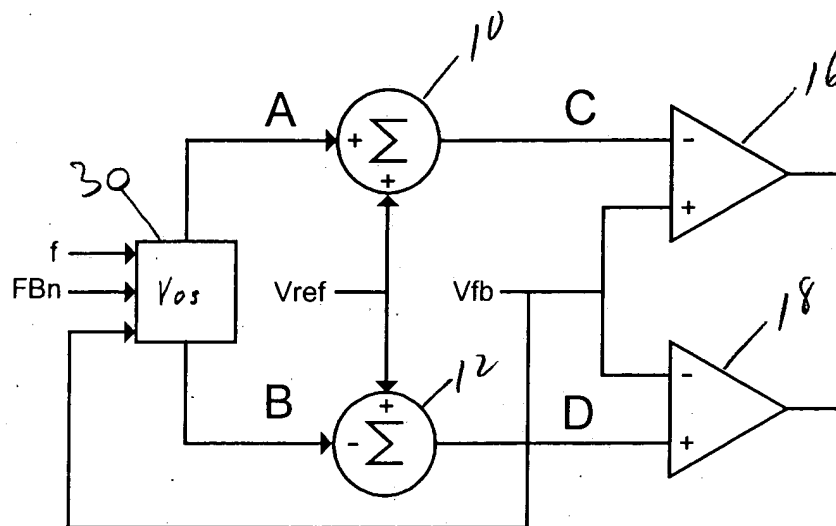


Fig. 9 ~~Improved Fixed Frequency Hysteretic Control~~
~~with Independent Window Limits~~

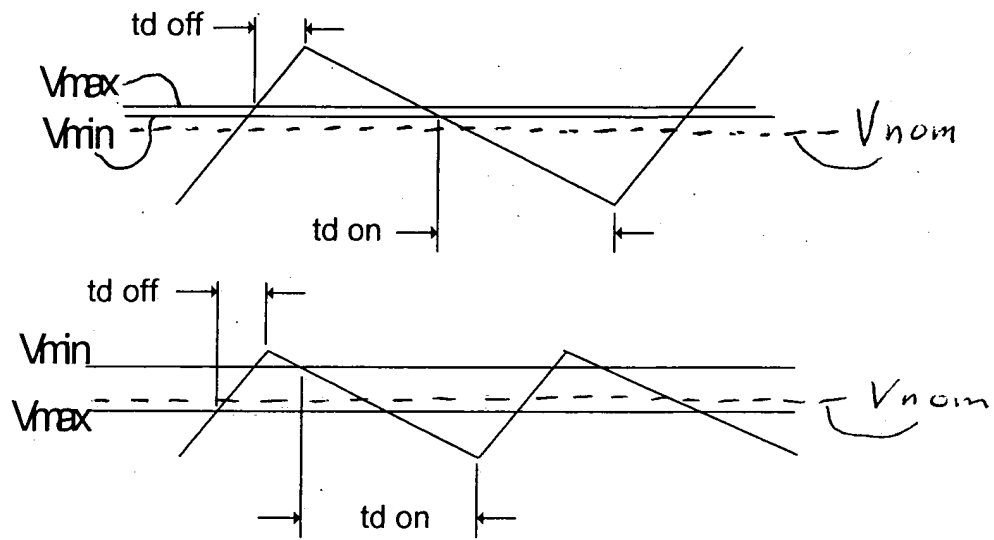


Fig. 10 ~~FFHC with V_{max} Less Than V_{min}~~